Rev 3 changelog:

1. Implemented ADC module to lab document specifications. Awaiting compiling / testing on uC
2. Implemented Timer module to lecture document specifications. Awaiting implementation to lab document specifications.

Rev 5 changelog:

1. Rewritten ADC code to comply with lab instructions
2. Reworked the calculation method to follow the trapezoidal method as presented to us in Duleepa’s MATLAB code.